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<b>1.</b>	High efficience reduction Shimoda variable specified. reproduce used as a signals to hereinafte Fig. 36 is Full text	A, Kenji / Takeda, Hitoshi, EUROPEAN PATENT, Dec 1992  e, the recording position of an intra-image coded image on a track cannot be For this reason, the intra-image coded image may not be edcontrast to this, in some method, only the intra-image coding scheme is codingsignals into a frame structure, and at the same time outputs the a bit rateappears after "0"s (to be referred to as a non-zero coefficient er). Thefor explaining this coding system. The macroblock signal shown in input to  available at patent office. For more in-depth searching go to LexisNexis-results from Patent Offices	Did you me macro blow intra" flag s mean"  Or refine a All of the Refine
<b>2.</b>	reduction Shimoda PATENT Azero-ru the color section 83 an error f the same macrobic	iency coding signal processing apparatus with error propagation influence  I, Kenji, Yokohama, JP / Takeda, Hitoshi, Yokohama, JP, UNITED STATES  IND TRADEMARK OFFICE GRANTED PATENT, Dec 1997  In/Amp calculating section 86, a non-zero coefficient coding section 87, andof difference system are the same as those of the zero-run/Amp calculating  In the non-zero coefficient coding section 84, andaddition, the circuit 91 adds and to data which cannot be corrected, anddata decoder 94, respectively. At a time, the signal separation circuit 92and outputs the macroaddress MBA, the ock pointer MBP, and the code length L constituting  available at patent office. For more in-depth searching go to LexisNexis-	

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